IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Chatterjee et al.

SERIAL NO.:   10/788,943

FILING DATE: February 26, 2004

TITLE: Memory Unit with Controller Managing Memory Access through
                 JTAG and CPU Interfaces

EXAMINER: Kerveros, James C

ART UNIT: 2138

ATTY. DKT. NO.: IDT-1872

MAIL STOP AMENDMENT
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

AMENDMENT AND RESPONSE

Dear Sir:

In response to the non-final Office Action mailed on February 21, 2007, having a
shortened statutory period set to expire on May 21, 2007, please enter the following
amendments in the above-identified Application and consider the following remarks.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Remarks begin on page 11 of this paper.